EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	20	(distance or spac$3) same separat$4 same (raster$5 or pixel or (bitmap$4 or (bit adj map$4))) and "716"/$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/09 12:14
L2	0	(distance or spac$3) same (raster$5 or pixel or (bitmap$4 or (bit adj map$4))) and "716"/$.ccls. and (CMOS same separat$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/09 07:48
L3	0	(distance or spac$3) same (raster$5 or pixel or (bitmap$4 or (bit adj map$4))) and "716"/$.ccls. and (CMOS same (separat$4 or "U"))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/09 07:49
L4	1063	(distance or spac$3) same (raster$5 or pixel or (bitmap$4 or (bit adj map$4))) and (CMOS same (separat$4 or "U"))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/09 07:50
L5	124	(distance or spac$3) same (raster$5 or pixel or (bitmap$4 or (bit adj map$4))) same (CMOS same (separat$4 or "U"))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/09 08:00
L6	27	(distance or spac$3) same (raster$5 or pixel or (bitmap$4 or (bit adj map$4))) same (CMOS same (separat$4 or "U")) and cellular	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/09 07:56
L7	0	((latch adj up) or latchup) same (check$3 or analyz$3 or analysis) same CMOS same (separat$3 or "U") and (distance or spac$3 or constraint or drc or (design adj rule))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/09 08:03
L8	218	((latch adj up) or latchup) same CMOS same (separat$3 or "U") and (distance or spac$3 or constraint or drc or (design adj rule))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/09 08:03
L9	13	((latch adj up) or latchup) same CMOS same (separat$3 or "U") and (distance or spac$3 or constraint or drc or (design adj rule)) and (raster$3 or (bit adj map$4) or bitmap$4 or pixel)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/09 09:59

L19	2	((latch adj up) or latchup) same CMOS same (separat$3 or "U") and (spac$3 near3 rule) and (raster$3 or (bit adj map$4) or bitmap$4 or pixel)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/09 10:04
L20	12	(spac$3 with rule with (cmos or (well adj contact) or (N adj2 well) or (N adj2 contact) or leg)) and (raster$7 or pixel or (bit adj2 map$4) or bitmap$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/09 10:20
L21	5	(spac$3 with rule with cmos) and (raster$7 or pixel or (bit adj2 map$4) or bitmap$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/09 11:07
L22	20	((latch adj up) or latchup) with spac$3 with distance	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/09 11:15
L23	20	((latch adj up) or latchup) with spac$3 with (distance or unreac$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/09 11:16
L24	4	((latch adj up) or latchup) with spac$3 with $2reach$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/09 11:18
L25	4	((latch adj2 up) or latchup) with spac$3 with $2reach$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/09 11:19
L26	22	((latch adj2 up) or latchup) same spac$3 same $2reach$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/09 14:41
L27	0	(cellular adj algorithm) and "716."$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/09 12:14
L28	9	(cellular adj algorithm)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/09 12:14

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L29	14	(((latch adj2 up) or latchup) same spac$3) and (raster$7 or pixel or (bit adj2 map$3) or bitmap$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/09 14:51
S1	2	"20050240886"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/07 09:18
S2	1	(vector near3 (graphic$3 or imag$3)) same (bitmap near3 (imag$3 or format$4)) and ((rule near3 check$3) or drc) and ((area or region or shape) near3 unreachable) and "716"/$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/07 09:22
S3	1	(vector near3 (graphic$3 or imag$3)) same (bitmap near3 (imag$3 or format$4)) and ((rule near3 check$3) or drc) and ((area or region or shape) near3 (unreachable or unpermit$4)) and "716"/$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/07 09:22
S4	1	(vector near3 (graphic$3 or imag$3)) same (bitmap near3 (imag$3 or format$4)) and ((rule near3 check$3) or drc) and ((area or region or shape) with (unreachable or unpermit$4)) and "716"/$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/07 09:23
S5	1	(vector near3 (graphic$3 or imag$3)) same (bitmap near3 (imag$3 or format$4)) and ((rule near3 check$3) or drc) and (unreachable or unpermit$4) and "716"/$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/07 09:23
S6	1	(vector near3 (graphic$3 or imag$3)) same (bitmap near3 (imag$3 or format$4)) and ((rule near3 check$3) or drc) and (unreachable or unpermit$6) and "716"/$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/07 09:23
S7	1	(vector near3 (graphic$3 or imag$3)) same (bitmap near3 (imag$3 or format$4)) and ((rule near3 check$3) or drc) and (unreachable or unpermit$6 or proxibit$6) and "716"/$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/07 09:24
S8	1	(vector same (graphic$3 or imag$3)) same (bitmap near3 (imag$3 or format$4)) and ((rule near3 check$3) or drc) and (unreachable or unpermit$6 or proxibit$6) and "716"/$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/07 09:25

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S9	195	raster$5 and "716"/$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/07 09:25
S10	10728	raster$5 and drcand "716"/$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/07 09:25
S11	26	raster$5 and drc and "716"/$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/07 09:27
S12	26	(raster$5 or (convert$3 with (graphic$3 or imag$3) with bitmap)) and drc and "716"/$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/07 09:51
S13	26	(raster$5 or (convert$3 with ((vector adj imag$3) or shape or (scalable adj object)) with (bitmap near3 imag$3))) and drc and "716"/$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/07 10:10
S14	1	(conductor adj region) with (contact adj shape) and "716"/$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/07 10:08
S15	2	(conductor near3 region) same (contact near3 shape) and "716"/$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/07 10:12
S16	26	(raster$5 or (convert$3 same ((vector near3 imag$3) or shape or (scalable adj object)) with (bitmap near3 imag$3))) and drc and "716"/$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/07 10:11
S17	26	(raster$5 or (convert$3 same ((vector near3 (imag$3 or graphic$3)) or shape or (scalable adj object)) with (bitmap near3 imag$3))) and drc and "716"/$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/07 10:12
S18	74	(raster$5 or (convert$3 same ((vector near3 (imag$3 or graphic$3)) or shape or (scalable adj object)) with (bitmap near3 imag$3))) and drc	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/07 11:08

S19	16	(conductor near3 region) same (contact near3 shape)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/07 10:13
S20	1	((conductor near3 region) same (contact near3 shape)) and raster$5	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/07 10:13
S21	1	(raster$5 or (convert$3 same ((vector near3 (imag$3 or graphic$3)) or shape or (scalable adj object)) with (bitmap near3 imag$3))) and drc and ((latch adj2 up) or latchup or thyristor)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/07 11:10
S22	2	(raster$5 or (convert$3 same ((vector near3 (imag$3 or graphic$3)) or shape or (scalable adj object)) with (bitmap near3 imag$3))) and drc and ((latch adj2 up) or latchup or thyristor or (current near3 leak$4))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/07 12:01
S23	1	(raster$5 or bitmap$4 or pixel) same layout and drc and ((latch adj2 up) or latchup or thyristor or (current near3 leak$4))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/07 12:16
S24	21	(raster$5 or bitmap$4 or pixel) same layout and drc and distance	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/07 12:17
S25	30	(raster$5 or bitmap$4 or pixel) same layout and drc and (distance or spac$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/07 14:14
S26	27	(raster$5 or bitmap$4 or pixel) same layout and drc and (distance or spac$3) and (cellular or cell or fill$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/07 14:23
S27	1	(raster$5 or bitmap$4 or pixel) and layout and drc and (distance or spac$3) and (cellular near3 algorithm)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/07 14:24

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S28	95	(raster$5 or bitmap$4 or pixel) and (cellular near3 algorithm)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/07 14:24
S29	1	(raster$5 or bitmap$4 or pixel) and (cellular near3 algorithm) and drc	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/07 14:25
S30	112	(raster$5 or bitmap$4 or pixel) and ((cellular near3 algorithm) or (fill near3 operation) or expand$3) and drc	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/07 14:26
S31	105	(raster$5 or bitmap$4 or pixel) and ((cellular near3 algorithm) or (fill near3 operation) or expand$3) and drc and (distance or spac$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/07 14:33
S32	29	(raster$5 or bitmap$4 or pixel) and ((cellular near3 algorithm) or (fill near3 operation) or expand$3) same (distance or spac$3) and drc	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/07 14:27
S33	7	(raster$5 or bitmap$4 or pixel) and ((cellular near3 algorithm) or (fill near3 operation) or expand$3) and drc and (distance or spac$3) and "716"/$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/07 14:45
S34	8	(raster$5 or bitmap$4 or pixel) and ((cellular near3 algorithm) or (fill near3 operation) or expand$3) and drc and "716"/$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/08 08:25
S35	1	(raster$5 or bitmap$4 or pixel) and ((cellular near3 algorithm) or (fill near3 operation) or expand$3) and drc and ((latch adj2 up) or thyristor) and "716"/$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/08 08:27
S36	1	(raster$5 or bitmap$4 or pixel) and drc and ((latch adj2 up) or thyristor) and "716"/$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/08 08:27
S37	11	drc and ((latch adj2 up) or thyristor) and "716"/$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/08 08:55

S38	28	drc and ((latch adj2 up) or thyristor or (current near3 leak$5) and (pixel or raster$3))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/08 09:08
S39	5	drc and ((latch adj2 up) or thyristor or (current near3 leak$5)) and (pixel or raster$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/08 08:57
S40	132	drc and ((latch adj2 up) or thyristor or (current near3 leak$5) and (pixel or raster$3) or (bit adj map$4) or bitmap$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/08 10:16
S41	20	drc and ((latch adj2 up) or thyristor or (current near3 leak$5) and (pixel or raster$3) or (bit adj map$4) or bitmap$4) and "716"/$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/08 09:10
S42	5	drc and ((latch adj2 up) or thyristor or (current near3 leak$5)) and ((pixel or raster$3) or (bit adj map$4) or bitmap$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/08 09:31
S43	5	drc and ((latch adj2 up) or thyristor or (current near3 leak$5)) and ((pixel or raster$3) or (bit adj map$4) or bitmap$4) and (distance or spac$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/08 09:32
S44	5	drc and ((latch adj2 up) or thyristor or (current near3 leak$5)) and (pixel or raster$3 or (bit adj map$4) or bitmap$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/08 10:16
S45	23	drc and (well adj contact) and (pixel or raster$3 or (bit adj map$4) or bitmap$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/08 10:19
S46	26	drc and ((well or substrate or butting) adj contact) and (pixel or raster$3 or (bit adj map$4) or bitmap$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/08 10:55
S47	215	drc and ("N" adj well) and (pixel or raster$3 or (bit adj map$4) or bitmap$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/08 11:01

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S48	65	drc same ("N" adj well) and (pixel or raster$3 or (bit adj map$4) or bitmap$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/08 10:58
S49	5	drc same ("N" adj well) and (pixel or raster$3 or (bit adj map$4) or bitmap$4) and (expand$3 or cellular)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/08 10:56
S50	3	drc and ("N" adj well) and (pixel or raster$3 or (bit adj map$4) or bitmap$4) and "716"/$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/08 11:04
S51	0	drc same CMOS same ((latch adj up) or latchup or thyristor or (current near3 leakage))) and (raster$5 or pixel or (bitmap$4 or (bit adj map$4))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/08 11:06
S52	3	drc same CMOS and ((latch adj up) or latchup or thyristor or (current near3 leakage))) and (raster$5 or pixel or (bitmap$4 or (bit adj map$4))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/08 11:09
S53	1944	CMOS and ((latch adj up) or latchup or thyristor or (current near3 leakage))) and (raster$5 or pixel or (bitmap$4 or (bit adj map$4))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/08 11:10
S54	373	(drc or (design near3 rule) or constraint) and CMOS and ((latch adj up) or latchup or thyristor or (current near3 leakage))) and (raster$5 or pixel or (bitmap$4 or (bit adj map$4))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/08 11:10
S55	83	(drc or (design near3 rule) or constraint) same CMOS and ((latch adj up) or latchup or thyristor or (current near3 leakage))) and (raster$5 or pixel or (bitmap$4 or (bit adj map$4))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/08 11:11
S56	83	(drc or (design near3 rule) or constraint) same CMOS and ((latch adj up) or latchup or thyristor or (current near3 leakage))) and (raster$5 or pixel or (bitmap$4 or (bit adj map$4)) and "716"/$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/08 11:52

S57	83	(drc or (design near3 rule) or constraint) same CMOS and ((latch adj up) or latchup or thyristor or (current near3 leakage))) and (raster$5 or pixel or (bitmap$4 or (bit adj map$4)) and unreachable and "716"/$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/08 11:48
S58	82	(drc or (design near3 rule) or constraint) same CMOS and ((latch adj up) or latchup or (thyristor near3 effect$3) or (current near3 leakage))) and (raster$5 or pixel or (bitmap$4 or (bit adj map$4)) and unreachable and "716"/$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/08 11:49
S59	82	(drc or (design near3 rule) or constraint) same CMOS and ((latch adj up) or latchup or (thyristor near3 effect$3) or (current near3 leakage))) and (raster$5 or pixel or (bitmap$4 or (bit adj map$4)) and unreachable and "716"/$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/08 11:50
S60	82	(drc or (design near3 rule) or constraint) same CMOS and ((latch adj up) or latchup or (thyristor near3 effect$3) or (current near3 leakage))) and (raster$5 or pixel or (bitmap$4 or (bit adj map$4)) and "unreachable" and "716"/$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/08 11:51
S61	82	(drc or (design near3 rule) or constraint) same CMOS and ((latch adj up) or latchup or (current near3 leakage))) and (raster$5 or pixel or (bitmap$4 or (bit adj map$4)) and "unreachable" and "716"/$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/08 11:51
S62	83	(drc or (design near3 rule) or constraint) same CMOS and ((latch adj up) or latchup or thyristor or (current near3 leakage))) and (raster$5 or pixel or (bitmap$4 or (bit adj map$4)) and "716"/$.ccls. and unreachable	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/08 11:53
S63	83	(drc or (design near3 rule) or constraint) same CMOS and ((latch adj up) or latchup or thyristor or (current near3 leakage))) and (raster$5 or pixel or (bitmap$4 or (bit adj map$4)) and "716"/$.ccls. and "unreachable"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/08 11:53

S64	5	(drc or (design near3 rule) or constraint) same CMOS same (dastance or spac$3) and ((latch adj up) or latchup or thyristor or (current near3 leakage))) and (raster$5 or pixel or (bitmap$4 or (bit adj map$4)) and "716"/$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/08 12:08
S65	6	(drc or (design near3 rule) or constraint) same CMOS same (distance or spac$3) and ((latch adj up) or latchup or thyristor or (current near3 leakage))) and (raster$5 or pixel or (bitmap$4 or (bit adj map$4)) and "716"/$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/08 12:14
S66	138	(drc or (design near3 rule) or constraint) same (distance or spac$3) and ((latch adj up) or latchup or thyristor or (current near3 leakage))) and (raster$5 or pixel or (bitmap$4 or (bit adj map$4)) and "716"/$.ccls. and CMOS	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/08 12:14
S67	138	(drc or (design near3 rule) or constraint) same (distance or spac$3) and ((latch adj up) or latchup or (thyristor near3 effect) or (current near3 leakage))) and (raster$5 or pixel or (bitmap$4 or (bit adj map$4)) and "716"/$.ccls. and CMOS	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/08 12:15
S68	138	(drc or (design near3 rule) or constraint) same (distance or spac$3) and ((latch adj up) or latchup or (thyristor near3 effect) or (current near3 leakage))) and (raster$5 or pixel or (bitmap$4 or (bit adj map$4)) and "716"/$.ccls. and CMOS and geometr$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/08 12:15
S69	1	(drc or (design near3 rule) or constraint) same (distance or spac$3) same (raster$5 or pixel or (bitmap$4 or (bit adj map$4)) and ((latch adj up) or latchup or (thyristor near3 effect) or (current near3 leakage))) and "716"/$.ccls. and CMOS	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/08 15:32

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S70	1	(drc or (design near3 rule) or constraint or (layout near3 (verif$3 or verification or test$3 or check$3 or evaluat$3))) same (distance or spac$3) same (raster$5 or pixel or (bitmap$4 or (bit adj map$4)) and ((latch adj up) or latchup or (thyristor near3 effect) or (current near3 leakage))) and "716"/$.ccls. and CMOS	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/08 15:36
S71	7	(drc or (design near3 rule) or constraint or (layout near3 (verif$3 or verification or test$3 or check$3 or evaluat$3))) same (distance or spac$3) and (raster$5 or pixel or (bitmap$4 or (bit adj map$4)) and ((latch adj up) or latchup or (thyristor near3 effect) or (current near3 leakage))) and "716"/$.ccls. and CMOS	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/08 17:09
S72	0	bitmap with drc and cmos	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/08 16:32
S74	1	((distance or spac$3) with greater) same (raster$5 or pixel or (bitmap$4 or (bit adj map$4))) and "716"/$.ccls. and CMOS	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/08 17:15
S75	12	(distance or spac$3) same (raster$5 or pixel or (bitmap$4 or (bit adj map$4))) and "716"/$.ccls. and CMOS	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/09 07:34

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